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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/824,678
	Filing Date	04/15/2004
	First Named Inventor	RUECKES et al.
	Art Unit	2812
	Examiner Name	TBA
Total Number of Pages in This Submission	Attorney Docket Number	112020.152 US2 NAN-28

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): - Postcard
Remarks 1. Form PTO-1449 (5 pgs.) 2. 35 Publications		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	Wilmer Cutler Pickering Hale and Dorr LLP		
Signature			
Printed name	Peter M. Diciara		
Date	03/30/2005	Reg. No.	38,005

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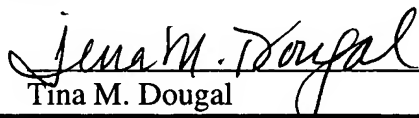
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: RUECKES et al.
Serial No.: 10/824,678 Examiner: TBA
Filed: April 15, 2004 Group Art Unit: 2812
For: **Process For Making Bit Erasable Devices Having Elements
Made With Nanotubes**
Atty. Docket No.: 112020.152 US2 NAN-28

CERTIFICATE OF MAILING UNDER 37 CFR 1.8(a)

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Sir:

Applicants and their legal representatives hereby make of record on the attached Form PTO-1449 the following publications which are known to them and considered warranting disclosure under 37 C.F.R. §1.56 and 1.97-98.

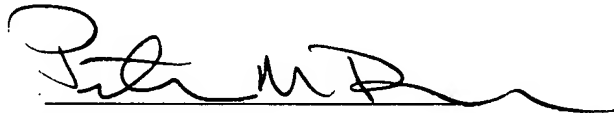
Copies of the publications listed on the attached Form PTO-1449, with the exception of U.S. patents and U.S. patent publications, are submitted herewith. It is respectfully requested that the Examiner initial and return a copy of the subject Form PTO-1449 with the next Patent Office communication.

The submission of these publications does not constitute a representation by the Applicants that a search has been made or that no better art exists and does not constitute an admission that the listed publications are material or constitute "prior art." Applicants reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed publications, should one or more of the publications be applied against the claims of the present application.

As the Information Disclosure Statement is being filed prior to the mailing date of the first Office Action on the merits, no fee is believed to be due. However, in the event a fee is due, please charge any fee deficiency or credit any overpayment to Deposit Account No. 08-0219.

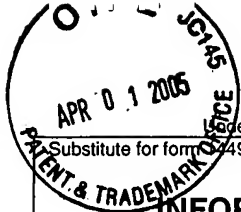
Respectfully submitted,

Dated: March 30, 2005



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Substitute for form PTO 449/PTO**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/824,678
Filing Date	April 15, 2004
First Named Inventor	RUECKES et al.
Art Unit	2812
Examiner Name	TBA
Attorney Docket Number	112010.152US2 NAN-28

Sheet	1	of	5
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U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2(f known)}			
		US 2002/0130311 A1	09-19-2002	LIEBER et al.	
		US 2002/0130353 A1	09-19-2002	LIEBER et al.	
		US 2002/0172963 A1	11-21-2002	KELLEY et al.	
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		US-4,876,667	10-24-1989	ROSS, et al.	
		US-4,888,630	12-19-1989	PATERSON	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/824,678
				Filing Date	April 15, 2004
				First Named Inventor	RUECKES et al.
				Art Unit	2812
				Examiner Name	TBA
Sheet	2	of	5	Attorney Docket Number	112010.152US2 NAN-28

	US-5,198,994	03-30-1993	NATORI	
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	US-6,809,465	10-26-2004	JIN	
	US-6,835,591	12-28-2004	RUECKES et al.	

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Sheet	3	of	5	Attorney Docket Number	112010.152US2 NAN-28

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2(If known)}			
		WO 01/44796 A1	06-21-2001	Board of Trustees of the Leland Stanford Junior University	
		WO 01/03208	01-11-2001	President and Fellows of Harvard College	
		WO 2003/091486	11-06-2003	Nantero, Inc.	
		WO 2004/065655	08-05-2004	Nantero, Inc.	
		WO 2004/065657	08-05-2004	Nantero, Inc.	
		WO 2004/065671	08-05-2004	Nantero, Inc.	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, volume- issue number(s), page(s), publisher, city and/or country where published.	T ²
	A1	AJAYAN, P.M., et al., "Nanometre-size tubes of carbon," Rep. Prog. Phys., 1997, Vol. 60, pp. 1025-1062.	
	A2	AMI, S. et al., "Logic gates and memory cells based on single C ₆₀ electromechanical transistors," Nanotechnology, 2001, Vol. 12, pp. 44-52.	
	A3	AVOURIS, Ph., "Carbon nanotube electronics," Carbon, 2002, Vol. 14, pp. 1891-1896.	
	A4	CASAVANT, M.J. et al., "Neat macroscopic membranes of aligned carbon nanotubes," Journal of Appl. Phys., 2003, Vol. 93(4), pp. 2153-2156.	
	A5	CHOI, W.-B. et al., "Carbon-nanotube-based nonvolatile memory with oxide-nitride-film and nanoscale channel," Appl. Phys. Lett., 2003, Vol. 82(2), pp. 275-277.	
	A6	CUI, J.B. et al., "Carbon Nanotube Memory Devices of High Charge Storage Stability," Appl. Phys. Lett., 2002, Vol. 81(17), pp. 3260-3262.	
	A7	DAI, H. et al., "Controlled Chemical Routes to nanotube Architectures, Physics, and Devices," J. Phys. Chem. B, 199, Vol. 103, pp. 111246-11255.	

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				Art Unit	2812
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Sheet	4	of	5	Attorney Docket Number	112010.152US2 NAN-28

	A8	DEHON, A., "Array-Based Architecture for FET-Based, Nanoscale Electronics," IEEE Transactions on Nanotechnology, 2003, Vol. 2(1), pp. 23-32.	
	A9	DEQUESNES, M. et al., "Calculation of pull-in voltages for carbon-nanotube-based nanoelectromechanical switches," Nanotechnology, 2002, Vol. 13, pp. 120-131.	
	A10	DEQUESNES, M. et al., "Simulation of carbon nanotube-based nanoelectromechanical switches," Computational Nanoscience and Nanotechnology, 2002, pp. 383-386.	
	A11	FAN, S. et al., "Carbon nanotube arrays on silicon substrates and their possible application," Physica E, 2000, Vol. 8, pp. 179-183.	
	A12	FARAJIAN, A. A. et al., "Electronic transport through bent carbon nanotubes: Nanoelectromechanical sensors and switches," Phys. Rev. B, 2003, Vol. 67, pp. 205423-1 = 205423-6.	
	A13	FISCHER, J.E. et al., "Magnetically aligned single wall carbon nanotube films: Preferred orientation and anisotropic transport properties," Journal of Appl. Phys., 2003, Vol. 93(4), pp. 2157-2163.	
	A14	FRANKLIN, N. R. et al., "Integration of suspended carbon nanotube arrays into electronic devices and electromechanical systems," Appl. Phys. Lett., 2002, Vol. 81 (5), pp. 913-915.	
	A15	FUHRER, M.S. et al., "High-Mobility Nanotube Transistor Memory," Nano Letters, 2002, Vol. 2(7), pp. 755-759.	
	A16	HOMMA, Y. et al., "Growth of Suspended Carbon Nanotubes Networks on 100-nm-scale Silicon Pillars," Appl. Phys. Lett., 2002, Vol. 81(12), pp. 2261-2263.	
	A17	KINARET, J.M. et al., "A carbon-nanotube-based nanorelay," Appl. Phys. Lett., 2003, Vol. 82(8), pp. 1287-1289.	
	A18	LEE, K.-H. et al., "Control of growth orientation for carbon nanotubes," Appl. Phys. Lett., 2003, Vol. 82(3), pp. 448-450.	
	A19	RADOSAVLIEVIC, M. et al., "Nonvolatile molecular memory elements based on ambipolar nanotube field effect transistors," Nano Letters, 2002, Vol. 2(7), pp. 761-764.	
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	A22	SOH, H. T. et al., "Integrated nanotube circuits: Controlled growth and ohmic contacting of single-walled carbon nanotubes," Appl. Phys. Lett., 1999, Vol. 75(5), pp. 627-629.	
Examiner Signature			Date Considered

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				Examiner Name	TBA
Sheet	5	of	5	Attorney Docket Number	112010.152US2 NAN-28

	A23	SREEKUMAR, T.V., et al., "Single-wall Carbon Nanotube Films", Chem. Mater. 2003, Vol. 15, pp. 175-178.	
	A24	TANS, S. et al., "Room-temperature based on a single carbon nanotube," Nature, 1998.Vol. 393, pp. 49-52.	
	A25	TOUR, J. M. et al., "NanoCell Electronic Memories," J. Am. Chem. Soc., 2003, Vol. 125, pp. 13279-13283.	
	A26	VERISSIMO-ALVES, M. et al., "Electromechanical effects in carbon nanotubes: Ab initio and analytical tight-binding calculations," Phys. Rev. B, 2003, Vol. 67, pp. 161401-1 = 161401-4.	
	A27	WOLF, S., Silicon Processing for the VLSI Era; Volume 2 – Process Integration, Multi-Level-Interconnect Technology for VLSI and ULSI, 1990, Section 4.3 Materials for Multilevel Interconnect Technologies, pp. 189-191, Lattice Press, Sunset Beach	
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	A29	ZHAN, W., et al., "Microelectrochemical Logic Circuits," J. Am. Chem. Soc., 2003, Vol. 125, pp. 9934-9935.	

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